ASSIGNMENT -I

Submission Date: 2nd Oct

Each question carries 5 Marks

1. Convert( AB19 )16 into Binary Number System. Write about Shift Registers in detail.
2. Simplify the following Boolean function using four variable K maps
3. F(w,x,y,z)= Σ(4 ,6, 7 15)
4. F(A,B,C,D)= Σ(2,3,12,13,14,15)
5. Implement the logic function f(w,x,y,z)=Σ(1,4,5,11,12,15) using decoder 4x 16
6. Realize the 4x16 Decoder using 2x4 Decoders.
7. Design a counter which counts the numbers 0, 1, 3, 5, 7 & 9 using Flips of your choice.
8. Implement following the Full Adder and Full Subtractor using Decoder and Multiplexer.
9. Implement the following logic function using 8x1 mux f(A,B,C,D)= Σ(0,2, 3, 6, 8, 9, 12, 14).
10. Design 4- bit odd parity generator.